

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"20040093193"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/03 12:50
L2	2	"6539267".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/03 14:01
L3	2	"6847854".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/03 14:01
S1	27	cell adj delay same simulation same library	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/03 11:20
S2	31	cell adj delay same simulat\$3 same library	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/26 19:03
S3	13	S2 and transistor same delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/26 19:03
S4	8	S3 and @ad<"20030221"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 13:57
S5	27	"703".clas. and PCI near3 (simulat\$3 or emulat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 11:28
S6	21	S5 and @ad<"20030225"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 11:29

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S7	12	S6 and memory near3 map\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 11:29
S8	51	"710".clas. and PCI near3 (simulat\$3 or emulat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 11:31
S9	10	S8 and memory near3 map\$4 same PCI	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 11:29
S10	9	S9 and @ad<"20030225"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 11:30
S11	0	S10 and ACPI	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 11:30
S13	3	S10 and (ACPI or BIOS)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 11:31
S14	9	S7 and (ACPI or BIOS)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 11:31
S15	0	S7 and (ACPI)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 11:31
S16	0	"714".clas. and PCI near3 (simulat\$3 or emulat\$3) same ACPI	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 11:32
S17	0	"717".clas. and PCI near3 (simulat\$3 or emulat\$3) same ACPI	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 13:16

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S18	2	"20040167756"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 13:16
S19	2	S18 and operation adj condition	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/28 13:30
S20	2	S18 and property adj variation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/28 13:31
S21	32	cell adj delay same simulat\$3 same library	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 15:55
S22	14	S21 and transistor same delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 13:57
S23	8	S22 and @ad<"20030221"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 13:57
S24	8	S23 and library	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 15:36
S25	5	S24 and (rise or fall near3 tim\$3) and (load near3 capacitance)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/28 15:46
S26	0	S25 and statistic\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/28 15:49
S27	2	S20 and statistic\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/28 15:47

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S28	0	S25 and deviation and distribution	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/28 15:54
S29	0	S25 and deviation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/28 15:49
S30	0	S25 and distribution	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/28 15:51
S31	0	S25 and distrib\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/28 15:52
S32	2	S25 and standard	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/28 15:52
S33	1	S21 and deviation and distribution	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/28 15:54
S34	5	cell adj delay same (deviation and distribution)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/28 15:55